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J. Matthew Zigmant

(Depositor's name)

J. Matthew Zigmant

(Signature)

9-6-05

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10786,370	02/24/2004	Dirk Reese	015114-067500US	3403

TITLE OF INVENTION: OVER-VOLTAGE PROTECTION OF INTEGRATED CIRCUIT I/O PINS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	10/14/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
ZWEIZIG, JEFFERY SHAWN	2816	327-112000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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Townsend and Townsend

1. and Crew LLP2. J. Matthew Zigmant

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

San Jose, CA

Altera Corporation

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Sept 6, 2005

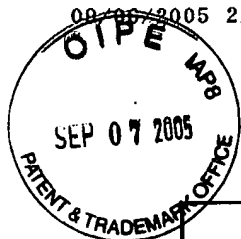
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In re: application of: Zweizig, Jeffrey S.

Application Number: 10/786,370

Filed: 02/24/2004

Title: Over-Voltage Protection of Integrated Circuit I/O Pins

Atty Docket Number: 015114-067500US JMZ/lo

Being faxed to Examiner - **Zweizig, Jeffery S.** Group **2816** at facsimile number **1-571-273-2885** are the following documents:

1. This PTO/SB/97 Certificate of Transmission (1 page); and
2. PTOL-85 Part B - Fee(s) Transmittal (1 page submitted in duplicate).

Number of pages being transmitted: 3

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